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 Very Large Scale Integration (VLSI) Systems, IEEE Transactions on  
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 Micro, IEEE  
 Volume 17, Issue 4, July-Aug. 1997 Page(s):67 - 76  
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 Manufacturing Engineer  
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 Graziano, M.; Masera, G.; Piccinini, G.; Roch, M.R.; Zamboni, M.;  
 Microelectronics, 1998. ICM '98. Proceedings of the Tenth International Conference on  
 14-16 Dec. 1998 Page(s):37 - 40  
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 Delaurenti, M.; Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
 Electronics, Circuits and Systems, 1999. Proceedings of ICECS '99. The 6th IEEE International Conference on  
 Volume 3, 5-8 Sept. 1999 Page(s):1671 - 1674 vol.3  
[AbstractPlus](#) | Full Text: [PDF\(312 KB\)](#) IEEE CNF
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 Graziano, M.; Masera, G.; Piccinini, G.; Ruo Roch, M.; Zamboni, M.;  
 Microelectronic Test Structures, 1999. ICMTS 1999. Proceedings of the 1999 International Conference on  
 15-18 March 1999 Page(s):68 - 73  
[AbstractPlus](#) | Full Text: [PDF\(748 KB\)](#) IEEE CNF

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Delaurenti, M.; Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
ASIC/SOC Conference, 2000. Proceedings. 13th Annual IEEE International  
13-16 Sept. 2000 Page(s):402 - 406  
[AbstractPlus](#) | Full Text: [PDF](#)(360 KB) IEEE CNF
8. **Transistors optimization of CMOS logic structures for high performance IC**  
Delaurenti, M.; Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Microelectronics, 1999. ICM '99. The Eleventh International Conference on  
22-24 Nov. 1999 Page(s):157 - 160  
[AbstractPlus](#) | Full Text: [PDF](#)(288 KB) IEEE CNF
9. **A noise test structure for CMOS logic families**  
Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Microelectronics, 1999. ICM '99. The Eleventh International Conference on  
22-24 Nov. 1999 Page(s):93 - 96  
[AbstractPlus](#) | Full Text: [PDF](#)(456 KB) IEEE CNF
10. **Noise safety design methodologies**  
Graziano, M.; Delaurenti, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Quality Electronic Design, 2000. ISQED 2000. Proceedings. IEEE 2000 First Internatio  
on  
20-22 March 2000 Page(s):157 - 164  
[AbstractPlus](#) | Full Text: [PDF](#)(240 KB) IEEE CNF
11. **Automated power supply noise reduction via optimized distributed capacitors in**  
Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Mixed-Signal Design, 2001. SSMSD. 2001 Southwest Symposium on  
25-27 Feb. 2001 Page(s):167 - 172  
[AbstractPlus](#) | Full Text: [PDF](#)(416 KB) IEEE CNF
12. **Switching noise analysis framework for high speed logic families**  
Delaurenti, M.; Graziano, M.; Masera, G.; Piccini, G.; Zamboni, M.;  
VLSI Design, 2001. Fourteenth International Conference on  
3-7 Jan. 2001 Page(s):524 - 530  
[AbstractPlus](#) | Full Text: [PDF](#)(692 KB) IEEE CNF
13. **Power supply wires self-heating analysis**  
Casu, M.; Graziano, M.; Roch, M.R.; Viglione, F.;  
Microelectronics, 2001. ICM 2001 Proceedings. The 13th International Conference on  
29-31 Oct. 2001 Page(s):115 - 118  
[AbstractPlus](#) | Full Text: [PDF](#)(472 KB) IEEE CNF
14. **Power bus optimal sizing in presence of power supply noise**  
Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Microelectronics, 2001. ICM 2001 Proceedings. The 13th International Conference on  
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 26-27 March 1990 Page(s):105 - 106  
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 Lapidoth, A.;  
 Information Theory, IEEE Transactions on  
 Volume 51, Issue 2, Feb. 2005 Page(s):437 - 446  
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- ☐ 3. **In-service monitoring for cell loss quality of service violations in ATM networks**  
 Hongbo Zhu; Frost, V.S.;  
 Networking, IEEE/ACM Transactions on  
 Volume 4, Issue 2, April 1996 Page(s):240 - 248  
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- ☐ 4. **Effects of heat and pressure on surface light reflectance at 660 and 880 nm**  
 Abrahams, J.; Bhattacharyya, S.K.; Ostrander, L.E.; Cui, W.;  
 Bioengineering Conference, 1991., Proceedings of the 1991 IEEE Seventeenth Annua  
 4-5 April 1991 Page(s):137 - 138  
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 Industrial Electronics, 1997. ISIE '97., Proceedings of the IEEE International Symposiu  
 7-11 July 1997 Page(s):773 - 778 vol.3  
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 Wan, Y.; Khalil, M.A.; Wey, C.-L.;  
 Computers and Digital Techniques, IEE Proceedings-  
 Volume 146, Issue 6, Nov. 1999 Page(s):295 - 301  
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